

JC914 U.S. PTO 09/745988 12/23/90		Subclass	Class	ISSUE CLASSIFICATION	PATENT NUMBER	
						
U.S. UTILITY Patent Application						
<i>JC2 807</i>			O.I.P.E. <i>h</i> SCANNED <i>JM3</i>	PATENT DATE <i>8w</i>		
APPLICATION NO. 09/745988		CONT/PRIOR F	CLASS 714	SUBCLASS 735	ART UNIT 2133	EXAMINER <i>Moise Decady</i>
APPLICANTS Dominique Bonneau Philippe Hauviller Vincent Vallet		<i>738</i>				
TITLE Built-in self test system and method for high speed clock and data recovery circuit						
PTO-2040 12/99						

BEST AVAILABLE COPY

ISSUING CLASSIFICATION

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.				NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				Amount Due	Date Paid
				ISSUE BATCH NUMBER	
<p>WARNING:</p> <p>The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368.</p> <p>Reception outside the U.S. Patent & Trademark Office is prohibited.</p>					

WARNING:

The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368. Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.